



Source: AGGIOS, Inc.

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30%-50% INSTANT POWER SAVINGS AT <\$10 AI TOKEN COSTS:

**AGGIOS INTRODUCES THE NEW ENERGYLAB PRO AI TOOL AND SEED PRO AI CONFIGURABLE FIRMWARE FOR
AI-DRIVEN SOC, FPGA AND BOARD POWER, PERFORMANCE AND THERMAL OPTIMIZATION**

**The new products are demonstrated at the 2026 Design Automation Conference on the Texas Instruments
Jacinto™ SoC for automotive and AMD Zynq® FPGA for video applications**

IRVINE, CA--(July 27, 2026) -- AGGIOS®, the industry leader in Software Defined Energy Management® is again lifting the technology bar for power, performance and thermal (PPT) optimizations of SoCs, FPGAs and boards by announcing two new AI-driven products the EnergyLab™ PRO AI tool and Seed™ PRO AI firmware.

The EDA industry, how we knew it since the 70s, is rapidly changing. The AI-driven reshaping of EDA is fastest at the post-silicon system design stage where the IP protection issues, as the sole remaining hurdle for the full deployment of leading AI LLMs, was the lowest. The AI-driven technology, products and business models of EDA are increasingly replaced with new products that guide and help human engineers to keep the pace with the smartness, experience and efficiency of their non-human AI counterparts, not the other way around, as it was the case in the past. With the help of our key industry partners AGGIOS is launching two new AI-driven products that are fulfilling the stringent PPT requirements of our common customers.

EnergyLab PRO AI allows users to quickly ingest, simulate and test the PPT characteristics of their SoC, FPGA or board designs to meet the product purpose. Tens of thousands of pages of complete hardware reference manuals, data sheets, application notes and application software source code are ingested by the selected single or multiple AI LLMs with smart token cost control to produce a detailed abstract PPT model of the hardware and software. The EnergyLab PRO AI tool allows highly accurate PPT simulations and exhaustive measurement and testing on the real hardware. The Seed PRO AI firmware and FPGA modules are executing on the processor and FPGA to optimize PPT behavior typically leading to 30-50% better power than standard optimizations by skilled engineers.

When: July 27-29th 2026

Where: Design Automation Conference, Long Beach CA, AGGIOS booth 755

What:

- Two new products EnergyLab PRO AI and Seed PRO AI demoed on:
 - Texas Instruments Jacinto™ SoC TDA4AEN front camera automotive application

- AMD Zynq® UltraScale+ MPSoC ZU7EV targeted reference design (TRD) video application
- Showcasing LLM ingestion, simulation, AI-driven hardware in the loop refinement, firmware optimization and verification

AGGIOS, Inc., Irvine, CA (<http://www.aggios.com>) provides AI-driven software defined platform management intellectual property, tools and services for mobile, plug load and internet of things electronic devices.

CONTACT INFORMATION

- AGGIOS contact: Vojin Zivojnovic, email: info@aggios.com, phone: +1 (949) 212-0130

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